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PATENT NUMBER and
ISSUE DATE

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U.S. UTILITY Patent Application

APPL NUM 10039138	FILING DATE 12/31/2001	CLASS 439	SUBCLASS 92	GAU 2833	EXAMINER GILMAN
**APPLICANTS: Bloomfield Rome; Fester Joseph;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO H052617.1141US0	
TITLE : Method and apparatus for grounding a processor board					

U.S. DEPT. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		DRAWING	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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